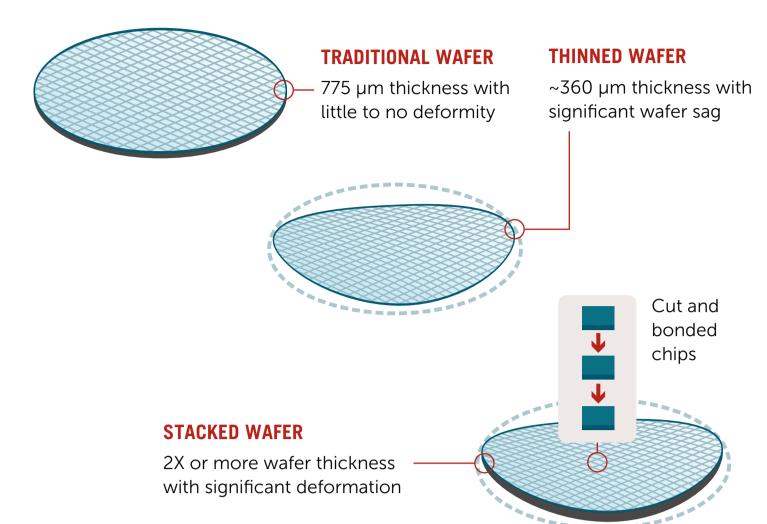
Advanced Back-End Packaging Solutions

Supporting Specialized Wafer Handling Needs

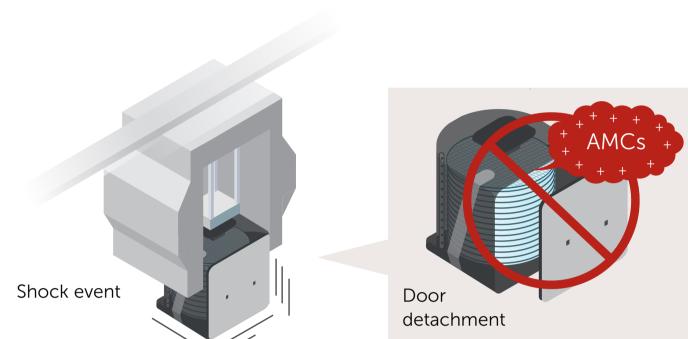
To enable 3D-IC and system-on-a-chip designs, device manufacturing involves increasingly complex processes of thinning wafers and stacking devices together to create complex architectures. Both thin wafers and stacked/bonded wafers present unique wafer handling challenges that traditional wafer handling products such as front opening unified pods (FOUPs) and wafer shippers struggle to handle. Here is how Entegris engineers have advanced back-end packaging solutions.

THE CHALLENGE: CHANGING WAFER SHAPES

A traditional wafer has the structural stability to stay flat. However, wafer thinning can reduce thickness by 50% or more, and stacked 3D-IC wafers may reach over three times their original thickness. Additionally, these wafers may deform due to lack of structural stability or built-in stress from the process required for device stacking.

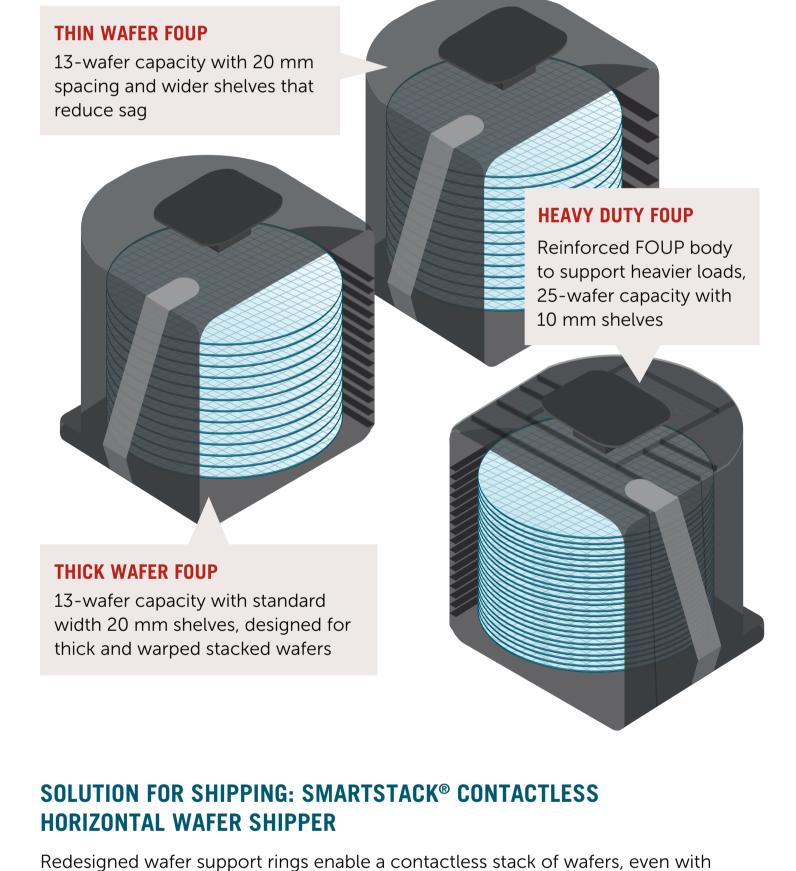


Additionally, stacked and bonded wafers weigh more than traditional wafers, which can lead to stress being placed on FOUP handling in overhead transport systems. A sudden shock or emergency stop event can lead to wafer damage, FOUP door breakage, and exposure to airborne molecular contaminants (AMCs).



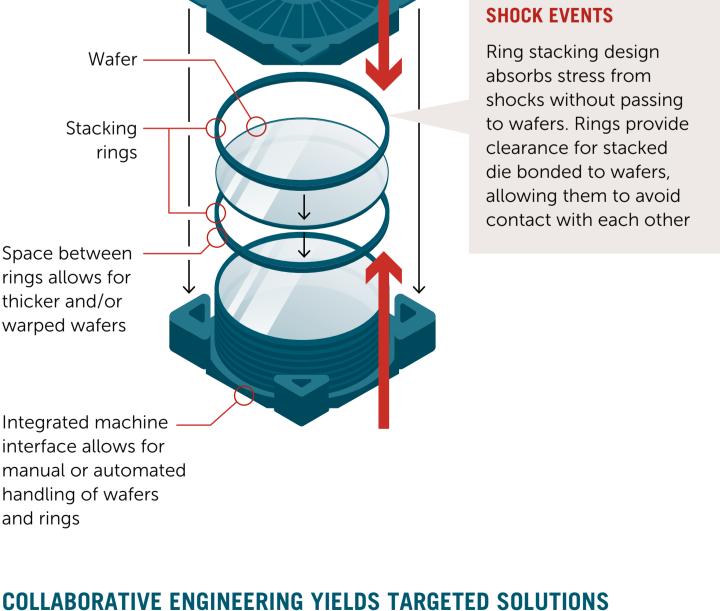
SOLUTION FOR FABS: ADVANCED A300 FOUPS FOR WAFERS New versions of the A300 FOUP are available that solve the problems of handling

thinned or stacked wafers. These versions are compatible with standard 300 mm load ports. All versions are compatible with inert gas purge systems.



increased thickness or warping.

PROTECTION FROM



handling challenges.

Learn More

As Entegris introduces new product lines, we welcome the opportunity to work directly with our customers and develop solutions that meet their unique wafer

www.entegris.com/advanced-packaging-solutions



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